

Abstract

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A radio frequency plasma display panel that is capable of reducing a discharge voltage. In the plasma display panel, a dielectric material is entirely coated on a substrate and is patterned to have a convex surface. A first electrode crossing the dielectric pattern is formed on the substrate, and a dielectric layer is entirely coated on the substrate provided with the dielectric pattern and the first electrode. A second electrode crossing the first electrode is formed on a concave groove area in the dielectric layer having a wave shape with lands and grooves.

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